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Docket No.: CIS0203US

November 20, 2006

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa,
Mark N. Yamashita and Yuval Bachar
Assignee: Cisco Technology, Inc.
Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK
BETWEEN VIAS IN A PRINTED CIRCUIT BOARD
Serial No.: 10/727,381
Examiner: Jeremy C. Norris
Docket No.: CIS0203US
Filed: December 4, 2003
Group Art Unit: 2841

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard;
- (2) This Transmittal Letter (1 page) *(in duplicate)*;
- (3) Response To Final Office Action (17 pages).

- ☒ No additional fee is required.
☐ The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>	<u>Rate</u>	Additional <u>Fee</u>
Total Claims	44	Minus	44	=	0	x \$ 50.00	\$ 0.00
Independent Claims	6	Minus	6	=	0	x \$200.00	\$ 0.00
<input type="checkbox"/> Fee of _____ for the first-filing of one or more multiple dependent claims per application							\$
<input type="checkbox"/> Fee for Request for Extension of Time							\$ 0.00
<u>Total additional fee for this Amendment:</u>							\$ 0.00

- ☒ Conditional Petition for Extension of Time: If an extension of time is required for timely filing of the enclosed document(s) after all papers filed with this transmittal have been considered, an extension of time is hereby requested.
- ☐ Please charge our Deposit Account No. 502306 in the amount of \$ 0.00
- ☒ Also, charge any additional fees required and credit any overpayment to our Deposit Account No. 502306.

Total: \$ 0.00

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 20, 2006.

Brenna A. Brock 11/20/2006
Attorney for Applicant(s) Date of Signature

Respectfully submitted,

Brenna A. Brock
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa, Mark N. Yamashita and Yuval Bachar

Assignee: Cisco Technology, Inc.

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

Application No.: 10/727,381 Filing Date: December 4, 2003

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COMMISSIONER FOR PATENTS
P. O. BOX 1450
ALEXANDRIA, VA 22313-1450

RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Final Office Action dated September 20, 2006, having a shortened statutory period expiring on December 20, 2006. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

No Amendments to the Specification of Drawings in this paper.

A listing of the claims begins on page 2 of this paper.

Remarks begin on page 12 of this paper.